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### [PDF] Turbo-ROB: A Low Cost Checkpoint/Restore Accelerator

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However, speculative execution requires a **checkpoint/restore** ..... A **low confidence branch** is given a GC if one is available, otherwise, a repair point is ...

www.eecg.toronto.edu/~moshovos/research/TurboROB.pdf

by P Akl - 2008 - Cited by 1 - Related articles

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**Low confidence branch. checkpoint. checkpoint. checkpoint.** ROB. Sometimes, it is better to wait if no **checkpoint** is available. Wait Scenario ...

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## Checkpoint Processing and Recovery: An Efficient, Scalable ...

CPR allocates a **checkpoint** only if a free **checkpoint** is available. If the processor fetches

a **low-confidence branch** and no **checkpoints** are available, ...

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**Selective low-confidence branch checkpoints.** The misprediction recovery mechanism must be fast and have low overhead. As discussed in Section 3.2, except ...

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**checkpoint.** CPR allocates a **checkpoint** only if a free **checkpoint** is available. If the processor fetch- es a **low-confidence branch** and no check- ...

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as soon as a **low-confidence branch** is encountered. When a thread context becomes free and a **low-confidence. checkpoint** already exists, the new context ...

## CPR0B: Checkpoint Processing with Opportunistic Minimal Recovery

**low-confidence branch** [15]—can be done in one cycle. Recovery to an un-checkpointed instruction is done by recovering to the immediately younger **checkpoint** ...

www.cs.upenn.edu/~amir/pubs/conf/cprob-pact09.pdf

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## An Analysis of a Resource Efficient Checkpoint Architecture

point (C1), we force a new **checkpoint** (C2) at the first **branch** (irrespective of whether it was a **low-confidence branch** or not). ...

portal.acm.org/ft\_gateway.cfm?id=1044826&type=pdf

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Still other processors **checkpoint** every **branch** instruction, ..... but may generate the confidence indicator to have **low confidence** if the confidence in the ...

[www.freepatentsonline.com/y2008/0148026.html](http://www.freepatentsonline.com/y2008/0148026.html)

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a single outstanding **checkpoint** at a time, and terminates the current speculative section

when a **low-confidence** prediction ...

[www.hpcacnt.org/acoma-papers/cava\\_taco.pdf](http://www.hpcacnt.org/acoma-papers/cava_taco.pdf)

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4.1 Selective **Low-Confidence Branch Checkpoints** ... tion of instructions associated with the **checkpoint**—the **counter** is incremented ...

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**counter** is less than a threshold, the **branch** is. **low-confidence** – note that a small **counter** size ... If a newly idle thread has a **low-confidence checkpoint**, ...

**Checkpoint Efficiency Using a Confidence Indicator - Patent ...**

The **counter** is incremented each time the corresponding **branch** is taken, .... but may generate the confidence indicator to have **low confidence** if the ...

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**[PDF] CAVA: Using Checkpoint-Assisted Value Prediction to Hide L2 Misses**

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hashed with some **branch** history bits. A 2-bit saturating **counter** selector predicts, based on the PC, which prediction to take. ...

www.hpcacconf.org/acoma-papers/cava\_taco.pdf

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In one embodiment, any **low confidence branch** may be assigned a **checkpoint** and

**counter** information about each **checkpoint** stored in a **checkpoint** buffer 224. ...

[www.patents.com/Scalable-rename-map-table-recovery/.../en-US/](http://www.patents.com/Scalable-rename-map-table-recovery/.../en-US/) - Cached

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**Checkpoint Efficiency Using a Confidence Indicator - Patent ...**

The **counter** is incremented each time the corresponding **branch** is taken, and decremented if not ... If the **confidence** is **low**, a **checkpoint** may not be saved. ...

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**[PDF] Threaded Multiple Path Execution**

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the **checkpoint** of a **branch** to start running the alternate-path ..... For a **low-confidence branch**, a **low** value is added to the **counter** for the newly forked ...

[www.princeton.edu/~rblee/ELE572Papers/ThreadedMultipathExecution.pdf](http://www.princeton.edu/~rblee/ELE572Papers/ThreadedMultipathExecution.pdf)

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